

RJK60S5DPE

600V - 20A - SJ MOS FET High Speed Power Switching

R07DS0639EJ0100 Rev.1.00 Apr 23, 2012

Features

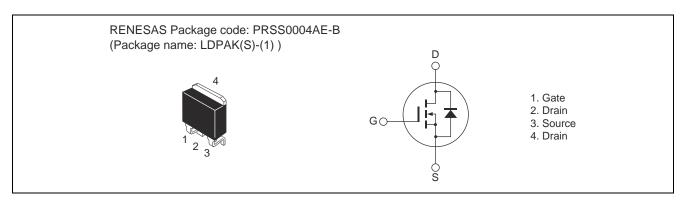
- Superjunction MOSFET
- Low on-resistance

 $R_{DS(on)} = 0.150 \Omega \text{ typ. (at } I_D = 10 \text{ A}, V_{GS} = 10 \text{ V}, Ta = 25 ^{\circ}\text{C})$

• High speed switching

 t_f = 23 ns typ. (at I_D = 10 A, V_{GS} = 10 V, R_L = 30 Ω , Rg = 10 Ω , Ta = 25°C)

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item		Symbol	Ratings	Unit
Drain to source voltage		V _{DSS}	600	V
Gate to source voltage		V_{GSS}	+30, -20	V
Drain current	Tc = 25°C	I _{D)} Note1	20	Α
	Tc = 100°C	I _{D)} Note1	12.6	Α
Drain peak current		I _{D (pulse)} Note1	40	Α
Body-drain diode reverse drain current		I _{DR} Note1	20	Α
Body-drain diode reverse drain peak current		I _{DR} (pulse) Note1	40	Α
Avalanche current		I _{AP} Note3	5	Α
Avalanche energy		E _{AR} Note3	1.36	mJ
Channel dissipation		Pch Note2	125	W
Channel to case thermal impedance		θch-c	1.0	°C/W
Channel temperature		Tch	150	°C
Storage temperature		Tstg	-55 to +150	°C

Notes: 1. Limited by Tch max.

- 2. Value at Tc = 25°C
- 3. STch = 25° C, Tch $\leq 150^{\circ}$ C

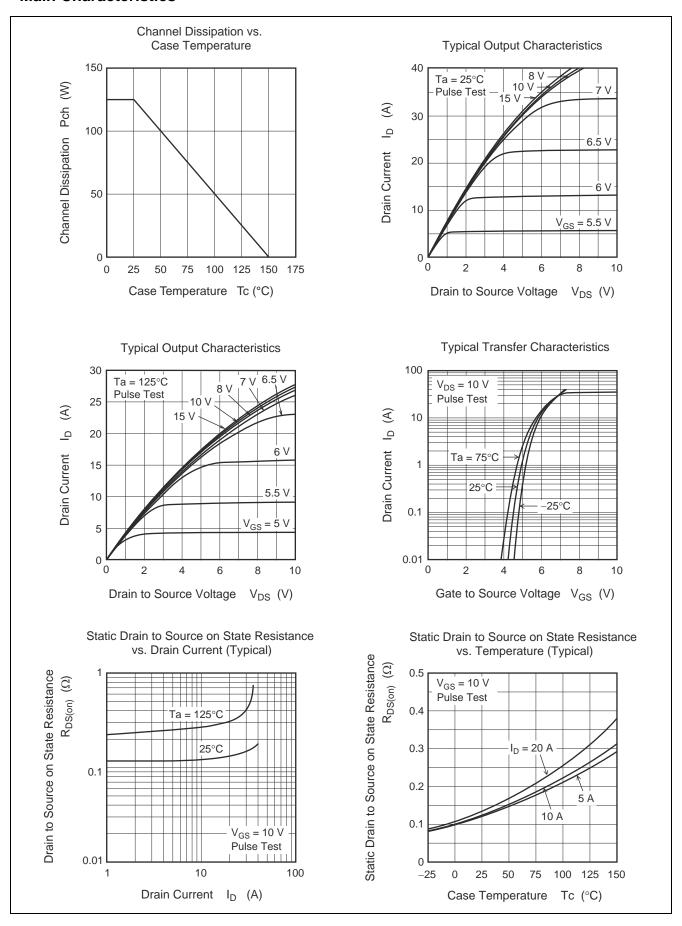
Electrical Characteristics

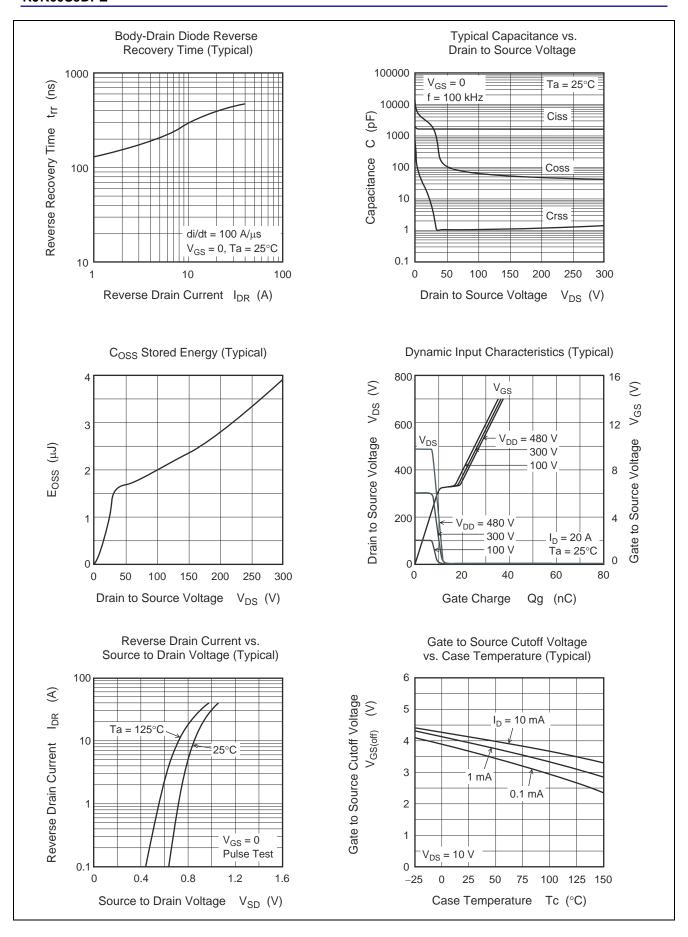
 $(Ta = 25^{\circ}C)$

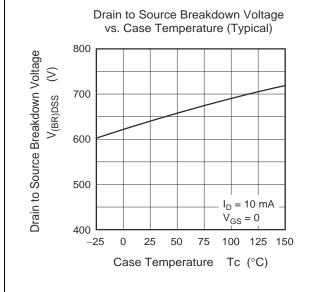
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	600	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	mA	V _{DS} = 600 V, V _{GS} = 0
Gate to source leak current	I _{GSS}	_	_	±0.1	μА	$V_{GS} = +30V, -20 V, V_{DS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	3	_	5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	0.150	0.178	Ω	$I_D = 10 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R _{DS(on}	_	0.375	_	Ω	Ta = 150°C
						$I_D = 10 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
Gate resistance	Rg	_	2.5		Ω	f = 1 MHz
						$V_{DS} = 25 \text{ V}, V_{GS} = 0$
Input capacitance	Ciss		1600	_	pF	$V_{DS} = 25 \text{ V}$ $V_{GS} = 0$ $f = 100\text{kHz}$
Output capacitance	Coss	_	2160		pF	
Reverse transfer capacitance	Crss	_	8.2	_	pF	
Turn-on delay time	t _{d(on)}	_	23	_	ns	$I_D = 10 \text{ A}$ $V_{GS} = 10 \text{ V}$ $R_L = 30 \Omega$ $Rg = 10 \Omega^{\text{Note4}}$
Rise time	t _r	_	25	_	ns	
Turn-off delay time	t _{d(off)}	_	49	_	ns	
Fall time	t _f	_	23	_	ns	
Total gate charge	Qg	_	27	_	nC	$V_{DD} = 480 \text{ V}$ $V_{GS} = 10 \text{ V}$ $I_D = 20 \text{ A}^{\text{Note4}}$
Gate to source charge	Qgs	_	10.5	_	nC	
Gate to drain charge	Qgd	_	8.5	_	nC	
Body-drain diode forward voltage	V_{DF}		0.96	1.60	V	I _F = 20 A, V _{GS} = 0 Note4
Body-drain diode reverse recovery time	t _{rr}		400	_	ns	I _F = 20 A
Body-drain diode reverse recovery	Irr	_	25	_	Α	$V_{GS} = 0$
current						$di_F/dt = 100 A/\mu s^{Note4}$
Body-drain diode reverse recovery	Qrr	_	5.6	_	μС	
charge						

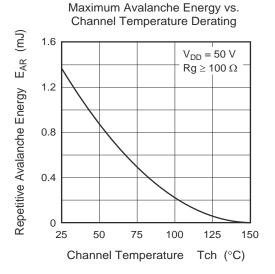
Notes: 4. Pulse test

Main Characteristics

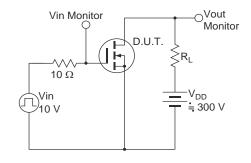


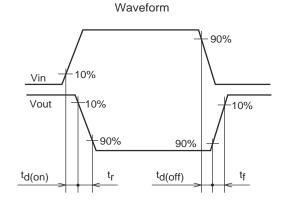




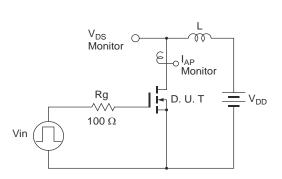


Switching Time Test Circuit

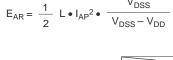


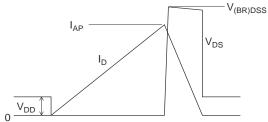


Avalanche Test Circuit

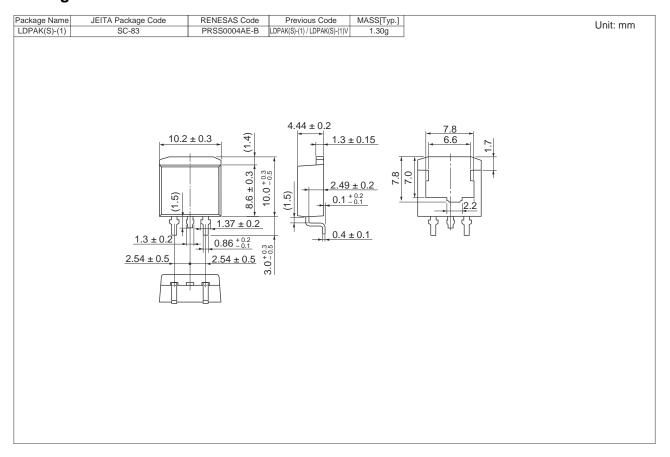


Avalanche Waveform





Package Dimension



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJK60S5DPE-00#J3	1000 pcs	Taping

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